



CSIR-Central Scientific Instruments Organisation (CSIR-CSIO)
Council of Scientific and Industrial Research (CSIR)
Sector 30-C, Chandigarh 160 030
<http://www.csio.res.in>



Advertisement No. 35/2021

[Filling up of temporary positions and preparing a waitlisted panel for future requirements against sponsored R & D projects]

Candidates who fulfill the under-mentioned criteria of age, educational qualifications, and experience etc. may fill the online Application Form through 'Recruitment' link on CSIR-CSIO website www.csio.res.in and upload the relevant documents (certificates of educational qualifications, experience, DOB, category etc.) in a single file on the online application portal on or before last date i.e. **11/10/2021**.

No hard copies of applications should be sent to this Office. Engagement will be purely on **contract/temporary basis and co-terminus** with the completion of the project. The exact date and time for holding physical/virtual interviews of the shortlisted candidates will be intimated on our website/intimated through e-mail in due course

S. No.	Tentative dates for following steps of selection	Days(Date)
1.	Last date for receipt of online applications (upto 5.00 p.m.)	11/10/2021
2	Tentative date for display of result of screening process (notifying list of shortlisted candidates to be called for interview)	14/10/2021
3	Tentative date for holding online interviews	18/10/2021
4	Tentative date for declaration of result (list of selected & Waitlisted candidates on website)	22/10/2021
5.	Tentative date for issuance of offer of engagement to selected candidate (with max. joining period 15 days)	25/10/2021

Before applying, the candidates are advised to go through the essential qualifications for their eligibility, as mentioned below (Table) for the positions being advertised and choose the positions for submitting the application. . In case of any technical difficulty in online application, please write to: vksehjal@csio.res.in with **screenshot of the error and Post Name/Post Code details. For eligibility etc., read the advertisement carefully.** The details of the present available position are as follows :-

Table: Present Requirement

S. No.	Name of the Scheme/Project	Positions & Stipend/Emoluments	No. of Positions	Tenure/ Project Duration (Tentative)	Essential Qualification in the area/discipline	Upper age limit as on last date of applications
1.	Externally Funded Project	<p>A. Project Associate-II :</p> <p>i. Rs. 35,000/- + HRA to Scholars who are selected through (a) National Eligibility tests- CSIR-UGC NET including lectureship (assistant professorship) or GATE or (b) A selection process through National level examination conducted by Central Govt. departments and their agencies and institutions.</p> <p>ii. Rs. 28000/- + HRA for others who do not fall under (i) above.</p>	01 (Electronics)	Up to June 2022	<p>Essential Qualification: B.Tech./B.E./M.Sc. in Electronics/ Electronics & Communication Engineering or Equivalent and two years experience[#] in Research and Development in Industrial and Academic institutions or Science and Technology Organisations and Scientific activities and services.</p> <p>Desirable:</p> <ul style="list-style-type: none"> • 02 years experience in the R & D / Industrial etc. • Experience in VLSI/Embedded System algorithm design. • Experience of FPGA System development. 	35 years ^{\$}

		<p>II. Project Associate-II :</p> <p>i. Rs. 35,000/- + HRA to Scholars who are selected through (a) National Eligibility tests- CSIR-UGC NET including lectureship (assistant professorship) or GATE or (b) A selection process through National level examination conducted by Central Govt. departments and their agencies and institutions.</p> <p>ii. 28000/- + HRA for others who do not fall under (i) above.</p>	01 (Computer/ Electronics)	<ul style="list-style-type: none"> • Experience in assembly and testing of SMDs and Leaded components on multilayer PCBs. • Experience in circuit simulation and synthesis. <p>Essential Qualification: B.Tech./B.E./M.Sc in Computer Science Engineering or in Electronics/ Electronics & Communication Engineering or Equivalent and two years experience[#] in Research and Development in Industrial and Academic institutions or Science and Technology Organisations and Scientific activities and services.</p> <p>Desirable:</p> <ul style="list-style-type: none"> • 02 years experience in the R & D / Industrial etc. • Experience in VLSI/embedded system design and testing. • Experience of FPGA System development. • Experience of hardware programming. 	35 years [§]
Total vacancies (Tentative)			02	--	--

[§]Upper age limit shall be reckoned as on the last date of receipt of applications.

[#]Experience: Experience shall be counted after obtaining the minimum prescribed qualification

*Candidates possessing essential qualification (E.Q.) in the required area/discipline **only** should apply, others are not eligible. Result Awaited candidates are not eligible.

Other Benefits: Accommodation (subject to availability), food facility and other amenities in campus.

Please note:

1. **The performance of the candidates selected against all the above-mentioned positions will be reviewed at regular intervals (3/6 months etc.) and based on the performance as per the project mandate, further extension (if applicable) will be provided from time to time.**
2. The candidates who have already served CSIR-CSIO or any other lab / institute of CSIR as any Project Assistant / Project Fellow / JRF (in contract R&D projects) / SRF (in contract R&D projects) / Research Associate etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure up to remaining period till completion of five years.
3. **Medical Facilities** for self will be provided through CSIR-CSIO Dispensary only to the extent available as per rules. This will be limited to self only and not for family members/dependents.
4. **Reservation:** As regards reservation, if all things are equal, SC/ST/OBC/PH candidates may be given preference over General candidates so as to ensure their representation.
5. In case a large number of candidates apply for the position, Selection/Screening Committee will have discretion to shortlist the candidates for interview based on written exam or percentage of marks or desirable qualification/experience or higher qualification or any other criteria deemed fit. In respect of equivalence clause in Essential Qualifications, if a candidate is claiming a particular qualification as equivalent qualification for recruitment as per the requirement of advertisement, then the candidate is required to produce order/letter in this regard, indicating the Authority (with number and date) under which it has been so treated otherwise the Application is liable to be rejected.
6. List of selected candidates will be displayed on website. In addition, a waiting panel will also be drawn for meeting future requirements of the project and it will be valid for one year.
7. The selected candidates are expected to join within fifteen days from the date of receiving offer of appointment, after completion of pre-appointment formalities like medical examination and character verification etc.
8. The selected candidates will have to deposit an amount equivalent to Fifteen Day's stipend as a Security Deposit (Refundable) with CSIR-CSIO, at the time of joining.

9. CSIR-CSIO reserves the right to cancel or withdraw the award in case of any discrepancy found, in the candidature of any selected candidate at any stage.
10. CSIR-CSIO reserves the right not to fill up a particular position, if it so desires. The number of vacancies indicated above may vary at the time of actual selection.
11. No TA will be paid to the candidates for attending the interview.
12. The engagement as a Research Intern (RI) (if applicable) under the 'CSIR Diamond Jubilee Research Intern Awards' Scheme is purely on temporary basis. The Intern shall have no claim for further extension, absorption or regularization in CSIR after the expiry/termination of the Internship. The engagement can be terminated by either side by giving one month's notice OR stipend in lieu thereof without assigning any reasons. It is not a CSIR-CSIO appointment and will not confer any right on the incumbent to any claim implicit or explicit on any position.
13. All the above positions in CSIR-CSIO are purely temporary contractual engagement co-terminus with the projects. The number of vacancies indicated above may increase/decrease.
14. The guidelines issued by CSIR Headquarters for the projects sponsored by CSIR shall be applicable as per CSIR OM dated 13 August, 2020 and 9th March, 2021 (available on www.csir.res.in). CSIR has adopted DST, GoI OM No. SR/S9/Z-05/2019 dated 10 July 2020 pertaining to guidelines and emoluments of project staff in R&D projects.
15. DST OM No. SR/S9/Z-08/2018 dated 30.01.2019 shall be applicable for DST sponsored projects for JRF/SRF.
16. CSIR Guidelines for Ethics in Research and in Governance, as made applicable vide CSIR OM No. 30/GB/2019-MD dated February 28, 2020 (available on csir.res.in) and amended from time to time shall apply.
17. The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position.
18. Other terms & conditions will be governed as per guidelines issued by the funding agency/CSIR for the engagement of above Project Staff/RI as amended from time to time.

Director, CSIR-CSIO